

**Notice of References Cited**

Application/Control No.

10/500,397

Applicant(s)/Patent Under  
Reexamination  
NICOLAI ET AL.

Examiner

Farras Abdelnour

Art Unit

2624

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,714,451	03-2004	Ooishi et al.	365/185.09
*	B	US-5,347,600	09-1994	Barnsley et al.	382/249
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Fatemi, O.; Panchanathan, S.P., "Fractal engine: an affine video processor core for multimedia applications," Circuits and Systems for Video Technology, IEEE Transactions on, vol.8, no.7, pp.892-908, Nov 1998
	V	Bhunja, S.K.; Ghosh, S.K.; Kumar, P.; Das, P.P.; Mukherjee, J., "Design, simulation and synthesis of an ASIC for fractal image coding," VLSI Design, 1999. Proceedings. Twelfth International Conference On, vol., no., pp.544-547, 7-10 Jan 1999
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.